

PATENT APPLICATION

Atty. Docket No.: 1482-138
 Applicant: Barrie Gilbert
 Filing Date: Filed Herewith

Serial No. Not Yet Assigned

Group: Not Yet Assigned

INFORMATION DISCLOSURE CITATION FORM PTO-1449 (Modified)

U.S. PATENT DOCUMENTS

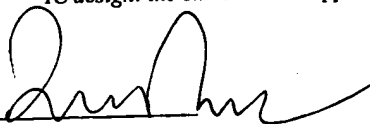
| <u>Exam Init</u> | <u>Ref</u> | <u>Document Number</u> | <u>Issue Date</u> | <u>Name</u> | <u>Class</u> | <u>Sub- Class</u> | <u>Filing Date</u> |
|----------------------|------------|----------------------------|-----------------------|-------------|--------------|-----------------------|------------------------|
| BT | --- | 4,250,457 | 02/1981 | Hofmann | 329 | 349 | |
| BT | --- | 5,909,136 | 06/1999 | Kimura | 327 | 356 | |

FOREIGN PATENT DOCUMENTS

| <u>Application Number</u> | <u>Date</u> | <u>Filing Date</u> | <u>Inventor</u> |
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OTHER DOCUMENTS

| <u>Exam Init</u> | <u>Ref</u> | <u>(Including Author, Title, Date, Pertinent Pages, Etc.)</u> |
|----------------------|------------|--|
| BT | --- | Gilbert, Barrie; <u>Novel Technique For R.M.S. - D.C. Conversion Based On The Difference Of Squares</u> ; <i>Electronics Letters</i> ; Vol. 11, No. 8; april 17, 1975; pages 181-182 |
| BT | --- | Gilbert, Barrie; <u>Current-mode Circuits From a Translinear Viewpoint: A Tutorial</u> ; <i>Analogue IC design: the current-mode approach</i> , 1990; pages 33-53 |

Examiner: 

Date Considered: 10/5/04